

Notice of Allowability	Application No.	Applicant(s)
	10/520,706	ONO ET AL.
	Examiner Aung S. Moe	Art Unit 2618

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. This communication is responsive to the communication filed on 11/15/2005.
2. The allowed claim(s) is/are ____.
3. Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) All b) Some* c) None of the:
 1. Certified copies of the priority documents have been received.
 2. Certified copies of the priority documents have been received in Application No. ____.
 3. Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: ____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.
THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

4. A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
5. CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 - 1) hereto or 2) to Paper No./Mail Date ____.
 - (b) including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date ____.

Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. Notice of References Cited (PTO-892)
2. Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. Information Disclosure Statements (PTO-1449 or PTO/SB/08),
Paper No./Mail Date see attached
4. Examiner's Comment Regarding Requirement for Deposit
of Biological Material
5. Notice of Informal Patent Application (PTO-152)
6. Interview Summary (PTO-413),
Paper No./Mail Date see attached.
7. Examiner's Amendment/Comment
8. Examiner's Statement of Reasons for Allowance
9. Other ____.

EXAMINER'S AMENDMENT

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with John R. Mattingly (Reg. No. 30,293) on 6/26/2006.

The application has been amended as follows:

In claim 1, line 16, the phrase “said sense register” has been changed to - - said sense **resistor** - -.

In claim 2, line 5, the phrase “a transfer side” has been replaced with - - said transfer side of said first and second current mirror circuits - -.

In claim 4, line 3, the phrase “said sense register” has been changed to - - said sense **resistor** - -.

In claim 10, line 4, the phrase “a modulated” has been changed to - - said modulated - -.

In claim 10, line 4, the phrase “a first” has been changed to - - said first - -.

In claim 10, line 7, the phrase “a modulated” has been changed to - - said modulated - -.

In claim 10, line 7, the phrase “a second” has been changed to - - said second - -.

In claim 14, line 3, the phrase “claim 12” has been changed to - - said claim 1 - -.

2. The following is an examiner's statement of reasons for allowance: claims 1-14 are considered allowable over the prior of record, since the prior art of record fails to show or fairly suggest an electronic component for amplifying high-frequency power having a first power amplification transistor for amplifying a modulated high-frequency signal in a first frequency band; a second power amplification transistor for amplifying a modulated high frequency signal in a second frequency band; a first transistor for output detection for receiving an input signal of the first power amplification transistor and a first current mirror circuit for passing current proportional to current of the transistor; a second transistor for output detection for receiving an input signal of the second power amplification transistor and a second current mirror circuit for passing current proportional to the current of the transistor; a sense resistor connected commonly on a transfer side of the first current mirror circuit and a transfer side of the second current mirror circuit and converting current to voltage; and a bias control circuit for comparing a signal detected by said sense resistor with an output request level instruction signal and generating a bias current for the first and second power amplification transistors as substantially described in claim 1.

Claims 15-17 are considered allowable over the prior of record, since the prior art of record fails to show or fairly suggest an electronic component for amplifying high-frequency power including a first power amplification transistor for amplifying a modulated high frequency signal in a first frequency band; a second power amplification transistor for amplifying a modulated high frequency signal in a second frequency band; a first transistor for output detection for receiving an input signal of the first power amplification transistor and a first current mirror circuit for passing current at a first ratio to current of the transistor; a second

transistor for output detection for receiving an input signal of the second power amplification transistor and a second current mirror circuit for passing current at a second ratio, which is different from said first ratio, to current of the transistor; and a converting circuit commonly connected to the first and second current mirror circuits and converting current to voltage, wherein a bias control circuit for comparing an output signal of said converting circuit with an output request level instruction signal and generating a bias current of the first power amplification transistor as substantially described in claim 15.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

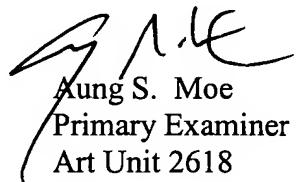
3. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. Following references are related to the present claimed invention:

US 20040229579A1 US 20050208905A1 US 20060012435A1 US 20050030100A1
US 20040108902A1 US006605999B2 US006919762B2 US 20050206456A1
US 20060068726A1

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Aung S. Moe whose telephone number is 571-272-7314. The examiner can normally be reached on Flex.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Edward F. Urban can be reached on 571-272-7899. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.



Aung S. Moe
Primary Examiner
Art Unit 2618

A. Moe
6/26/2006